**Solutions for KT1025A crash or reset**

**I. Chip Reset and Watchdog Description**

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| 1KT1025A or KT1025B chips do not bind the reset foot.So there is no special reduction foot. |
| 2The chip uses the way of power-on automatic reset and watchdog. |
| 3Watchdog What our internal program does is automatically reset after 8 seconds of crash.And the independent RC oscillation clock inside the chip is used, so the error is uncertain between 6 and 12 seconds.But it must be a stand-alone clock, a stand-alone module. |
| **4.**So chips don&apos;t crash easily.But in the event of that, our advice is to control the power supply of the chip.  **(1) Civil products, I think can be ignored.After all, the environment is simple and crash is basically impossible.**  **(2) Industrial products, I suggest to do something about them.** |

**2. Suggested Circuit - Control Chip Power Supply**

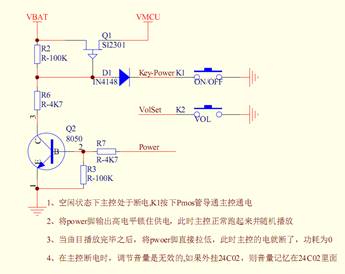
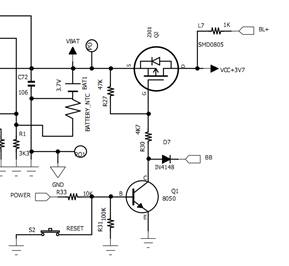


Figure 1 [left] illustrates as follows:**The same is true for reference.**

1. VBAT is the positive pole of the battery, that is, the power input of the whole system.

2. VMCU is the positive pole of power supply, which is the power supply entrance of the whole system.These two are controlled by the MOS transistor SI2301 shown above.

3. Q2 8050 drives MOS transistor, the core of which is Power foot, which is connected to MCU.

4. The figure above constitutes a soft-switching circuit. When you press the button, the main control turns on electricity.When you need to shut down, just pull Power down.

**These are just borrowed from our other documents.In fact, we can use a MOSFET to control the power supply of the chip.**